



Electronic Information Disclosure Statement

Integrated Circuit Package and Printed Circuit Board Arrangement

Application: 10/065016
Confirmation: 5731
Applicant(s): Olivier Boireau
Docket Number: 71522-2
Group Art Unit:
Examiner: Lourdes C. Cruz
search string: (4994902).pm.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

| Init | Citation No. | Patent Number | Date | Bar Code | Patentee | Class | Subclass |
|------|--------------|---------------|------------|----------|-----------------|-------|----------|
| | P01 | 4994902 | 1991-02-19 | | Okahashi et al. | | |

Signature

| Examiner Name | Date |
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